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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/614,426	07/07/2003	Stephen D. Russell	84892	8686
32697 7	590 12/14/2004		EXAM	INER
OFFICE OF PATENT COUNSEL SPAWARSYCEN, CODE 20012 53510 SILVERGATE AVE. ROOM 103 SAN DIEGO, CA 92152-5765			AKKAPEDDI, PRASAD R	
			ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 12/14/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		AR	
	Application No.	Applicant(s)	
	10/614,426	RUSSELL ET AL.	
Office Action Summary	Examiner	Art Unit	
	Prasad R Akkapeddi	2871	
The MAILING DATE of this communication a Period for Reply	ppears on the cover sheet with	the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory perions - Failure to reply within the set or extended period for reply will, by stat - Any reply received by the Office later than three months after the mail - earned patent term adjustment. See 37 CFR 1.704(b).	I. I. 136(a). In no event, however, may a regepty within the statutory minimum of thirty and will apply and will expire SIX (6) MONT ute, cause the application to become ABA	ly be timely filed 30) days will be considered timely. 4S from the mailing date of this communication. NDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 2a) This action is FINAL . 2b) ▼ The string This action is FINAL . 2b) ▼ The string This action is in condition for allow closed in accordance with the practice under	nis action is non-final. vance except for formal matte		
Disposition of Claims			
4) Claim(s) 1-17 is/are pending in the application 4a) Of the above claim(s) is/are withdress 5) Claim(s) is/are allowed. 6) Claim(s) 1-17 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and	rawn from consideration.		
Application Papers			
 9) The specification is objected to by the Exami 10) The drawing(s) filed on 07 July 2003 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction. 11) The oath or declaration is objected to by the 	a)⊠ accepted or b)□ objectence drawing(s) be held in abeyand ection is required if the drawing(s	e. See 37 CFR 1.85(a).) is objected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a lie	ents have been received. ents have been received in Apriority documents have been reau (PCT Rule 17.2(a)).	plication No eceived in this National Stage	
Attachment(s)	<u> </u>		
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/O Paper No(s)/Mail Date 07/07/2003. 	— . ·	mmary (PTO-413) Mail Date. <u>11/17/2004</u> . ormal Patent Application (PTO-152)	

Application/Control Number: 10/614,426

Art Unit: 2871

DETAILED ACTION

Double Patenting

1. Claims 1-9 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-9 of U.S. Patent No. 6,521,950 in view of Wanlass (U.S.Patent No. 3,881,175).

The only difference between the instant claims and those of the U.S.Patent 6,521,950 is the formation of "self-aligned circuitry" as recited in claim 1 of the instant application.

However, self-aligned circuitry on Silicon on Sapphire system is quite well known as taught by Wanlass (col. 3, lines 32-38).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to adapt the self-aligned circuitry technique as taught by Wanlass to the instant device to provide a reliable method for fabricating high-speed, high-density memory subsystems that is relatively inexpensive which will also decrease bonding requirements and lends itself to automatic fabrication (col. 2, lines 18-26).

2. Claims 10-17 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-8 of U.S. Patent No. 6,617,187 and claim 7 of U.S.Patent No. 6521,950 in view of Merriam Webster College Dictionary, Tenth Edition, P 457.

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The main difference between the instant claim 1 and that of U.S.Patent No. 6,617,187 is the step of 'affixing' vs. 'forming' a sapphire substrate to a single crystal (epitaxial) silicon structure).

The word 'form' is defined by the Merriam Webster College Dictionary as: 'to arrange themselves', 'to arrange in order'.

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to adapt the word 'forming' in place of 'affixing' such that the sapphire substrate and the single crystal silicon structure can be arranged in an order (see Merriam Webster College Dictionary, P 457).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prasad R Akkapeddi whose telephone number is 571-272-2285. The examiner can normally be reached on 7:00AM to 5:30PM M-Th.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert H Kim can be reached on 571-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PRA

Prasad R Akkapeddi, Ph.D Examiner
Art Unit 2871

TARIFUR R. CHOWDHURY
PRIMARY EXAMINER